
TIRUPATI RAO KALIPINDI,
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To pursue a challenging and stimulating career that will help me build a firm edifies on top of the strong foundation that I have acquired in my studies.

Present Position : **Associate Professor** in Praveenya Marine Engineering College.

S.No.	Name of the Institution	Place	Designation	Duration	
				From	To
1.	PRIME College	Vizianagaram	Associate Professor	01.07.2015	Till Date
2.	PRIME College	Vizianagaram	Associate Professor	22.06 2010	30.06.2015
3.	VITAM College	Visakhapatnam	Assistant Professor	02-04-2008	21-10-2008

Education:

2008-2011 M.Tech. In **VLSI System Design** from St.Therissa Institute of Technology & Management, Garividi, Vizianagaram, Andhra Pradesh, India.

2004-2008 B.Tech. in **Electronics & Communication Engineering** at JNTU Kakinada, Andhra Pradesh, India.

Achievements:

Received "**Appreciation letter**" from management of **VITAM** College of Engineering for the work done in **NBA**.

Research Publications:

International Journals:

- K.Tirupathi Rao, J.Sudhakar & B.Suresh* "Glitch Power Minimization Techniques in Low Power VLSI Circuits" proceedings of International Journal of Emerging Technology and Advanced Engineering, ISSN 2250-2459, Volume 2, Issue 11, November 2012.
- K.Tirupathi Rao & J.Sudhakar, "Implementation of non blocking NOC communication link using BTDS CDMA," Paper ID 116- ICCSP2012.

EXAMINATION WORKS & OTHERS:

- Acting as **Chief Superintendent** of University Examinations in PRIME College of Engineering.
- Acted as **observer** for EAMCET – 2016.

3. Acted as **observer** for APSLPRB – 27TH NOVEMBER, 2016.
4. Acted as **observer** for APSLPRB – 6TH NOVEMBER, 2016.
5. Acted as **observer** for EAMCET – 2014.
6. Acted as Vice Principal in PRIME College of Engineering.
7. Chief Examiner for PDC at Spot valuation centre.
8. Paper setter for Osmania University.
9. Prepared Mechatronics Engineering Syllabus in PRIME Engineering College under A.U.
10. Done paper valuation for UG theory subjects of JNTUK and Andhra University.

WORKSHOPS/SHORT TERM COURSES/ SEMINARS ATTENDED:

- Two days National Level Seminar on Recent trends in NANO technology, at G V P College of Engineering (Autonomous), Visakhapatnam in June 2013.
- One day Complimentary Seminar on “MATLAB & Simulink for Engineering Education” from Math Works India at Visakhapatnam on 7th February 2013.
- Two days National Level Seminar on “VLSI & Embedded Systems,” at VITAM College of Engineering, Visakhapatnam from December 2009.
- Two days National Workshop on “Signal processing Evolutionary computational and modeling,” at DIET Engineering College, Anakapalle from 14&15, March 2009.
- Two days workshop on “Recent Developments in information and Communications Technologies,” at AITAM Engineering College, Tekkali, Srikakulam from 12&13, Sept 2008
- Two days Workshop on “Embedded system and practice,” at Sri Prakash College of Engineering, Raju Pet, Tuni from 9&10, Aug 2008.
- Fifteen days National Level workshop on Mechatronics & Robotics, at NIT Hamirpur Himachal Pradesh in July 2008.

WORKSHOPS / PAPER CONTEST / COURSES / CONFERENCES ORGANIZED

1. One week National Level Faculty Development Program on “Advanced Digital Signal Processing and Applications” jointly organized by Electronics & ICT Academy, NIT Warangal & Department of ECE, MVGR College of Engineering (A), Vizianagaram & during 8th – 13th November 2016.
2. Two days “National Level Student Technical Paper Contest SPARCS – 2013 as a Coordinator held at PRIME College of Engineering, Vizianagaram”.
3. Two days “National Level Student Technical Paper Contest PRIMEXIBTs – 2012 as a Convener held at PRIME College of Engineering, Vizianagaram”.
4. Faculty Development program on “Signal Processing and Communication” organized by Department of ECE, VITAM Engineering College during 7– 8 July 2009 as Coordinator.
5. Two days “National Level Student Technical Paper Contest EMISSION – 2009” as a Coordinator held at VITAM College of Engineering, Visakhapatnam.

TEACHING TO UNDER GRADUATE CLASSES & LABS:

Subjects Handled:

1. Pulse & Digital Circuits
2. Electronic Devices & Circuits
3. Digital Signal Processing
4. Linear Integrated Circuit Applications
5. Digital Integrated Circuit Applications
6. Signals & Systems
7. VLSI

Labs Handled:

1. Electronic Devices & Circuits
2. Pulse & Digital Circuits
3. Linear Integrated Circuits
4. Electronic Circuits Analysis
5. Digital ICs and DSP Lab
6. AC & DC
7. Micro Processors & Interfacing

GUIDING RESEARCH:

1. Number of Projects guided for B.Tech /B.E students : 24
2. Number of Mini-Projects Guided : 09

Declaration:

I hereby declared that the above stated information is true and correct to best of my knowledge.

Date :

Place : VISAKHAPATNAM